

FIG. 1

100045629 - 102504

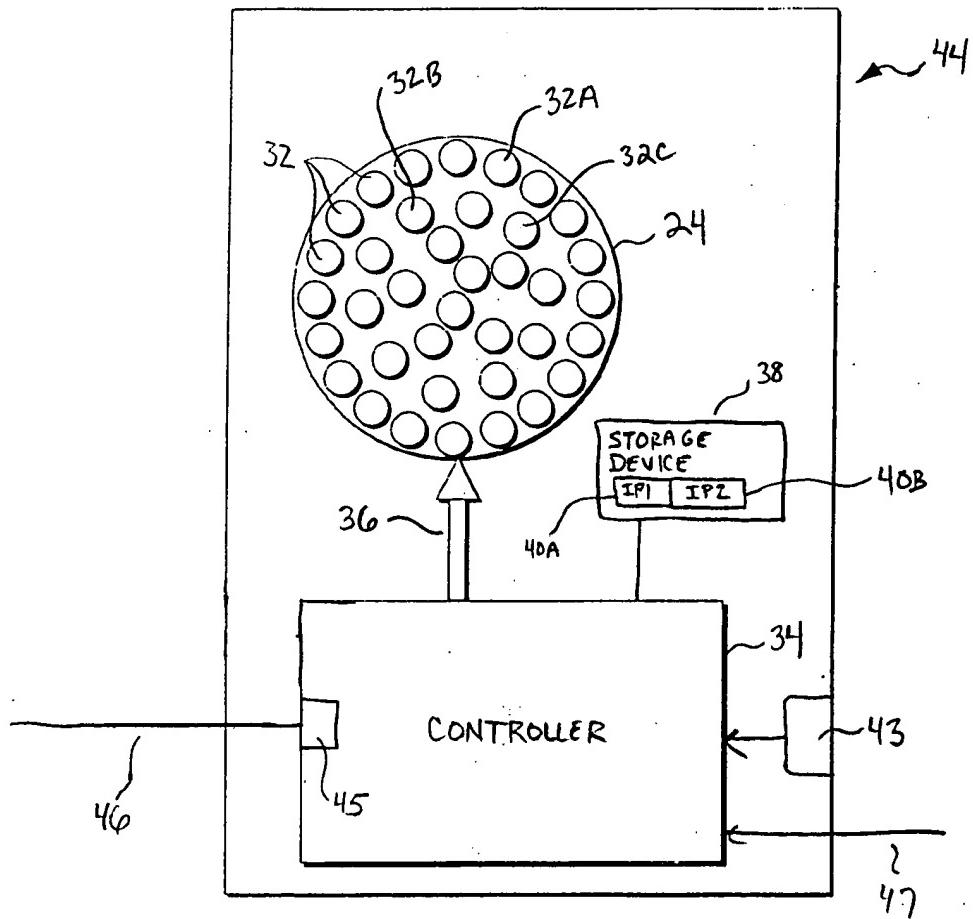


Fig. 2

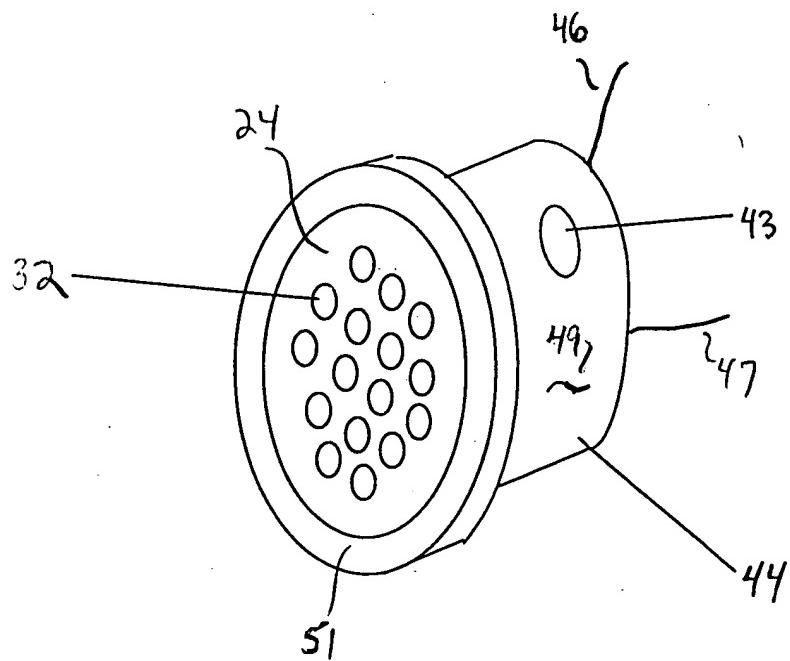


FIG. 3

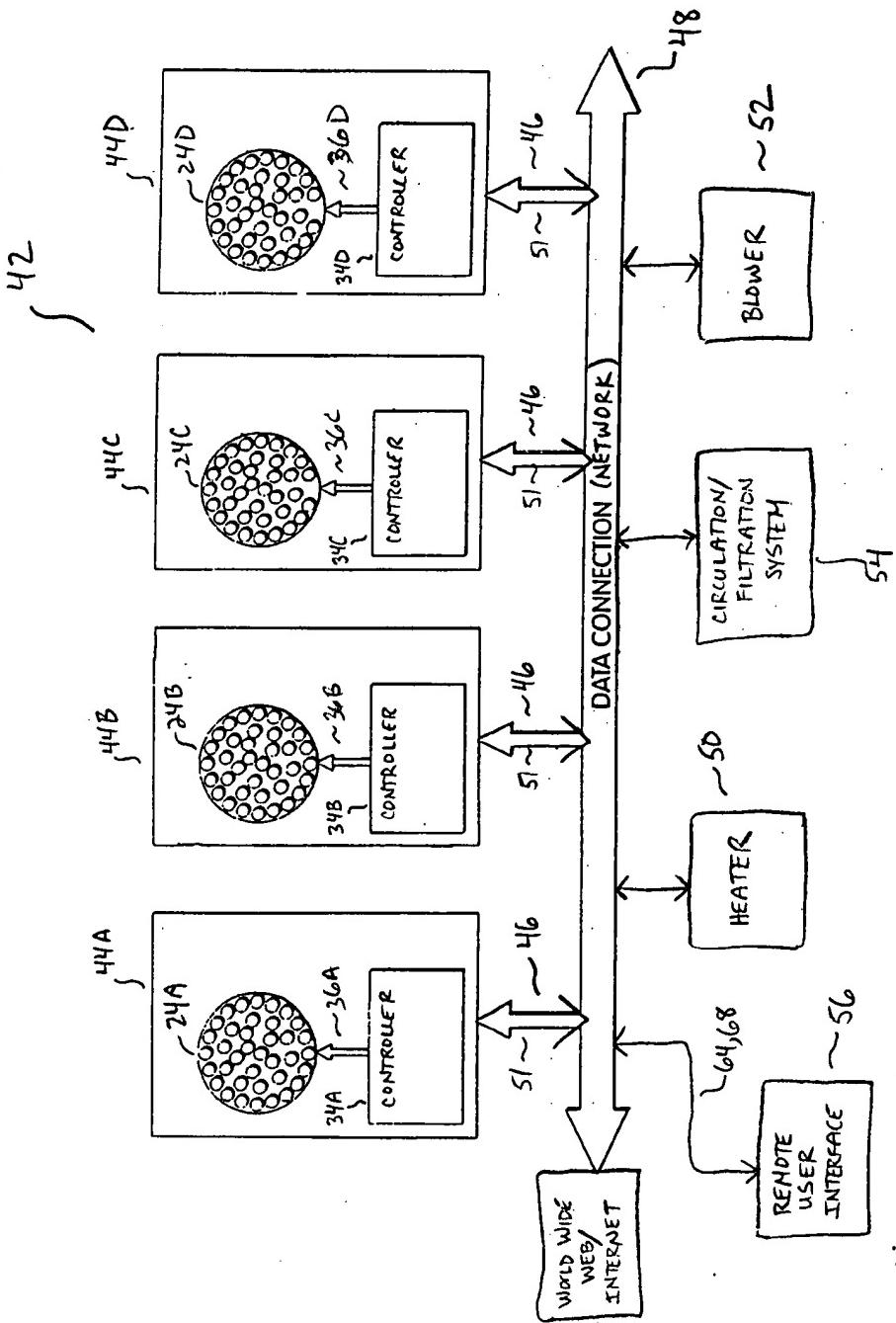


FIG. 4

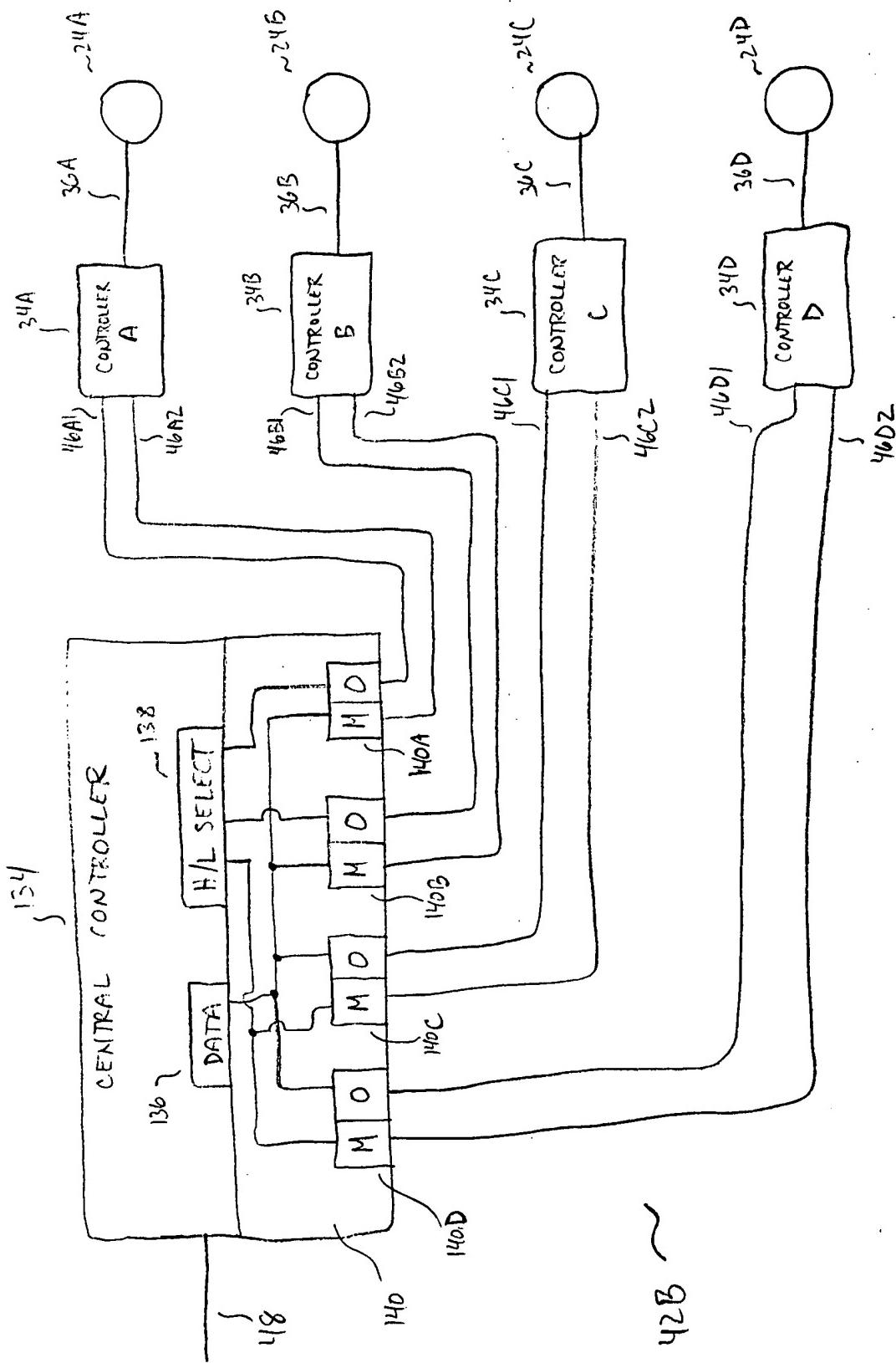


FIG. 4A

CONTROLLER	MODE (M)	OPTION (O)
A	DATA	LOGIC HIGH
B	DATA	LOGIC LOW
C	LOGIC HIGH	DATA
D	LOGIC LOW	DATA

FIG. 4B

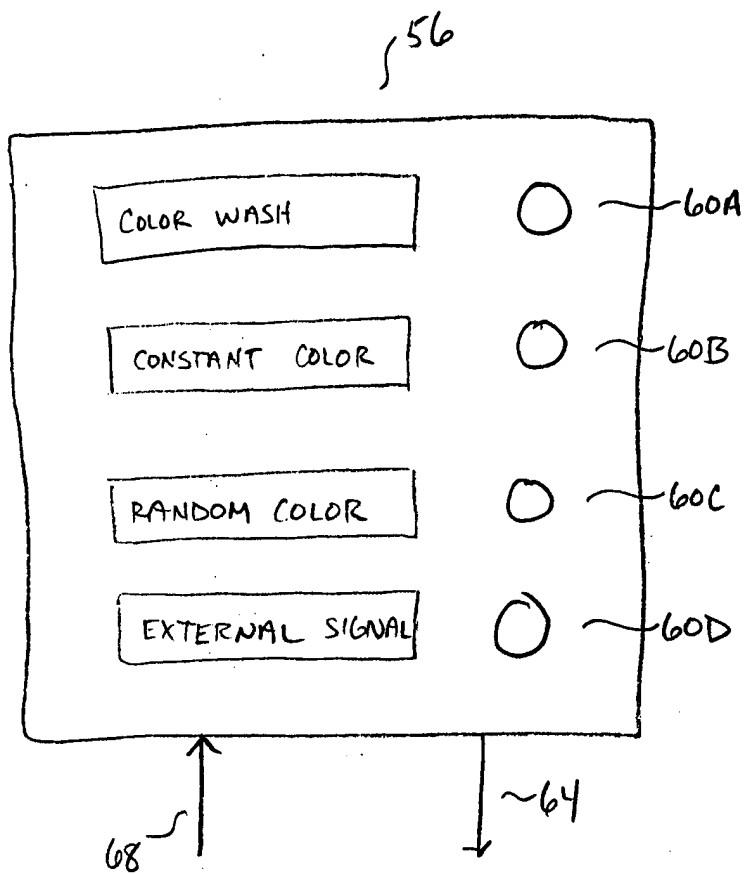


FIG. 5

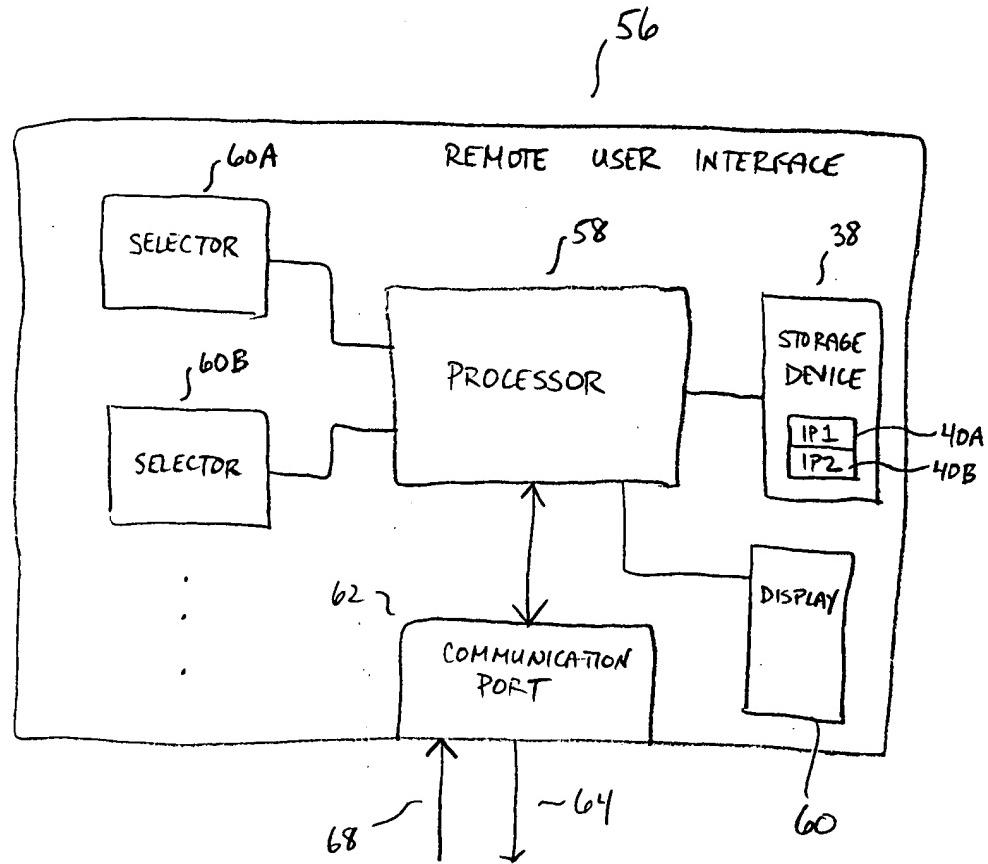


FIG. 6

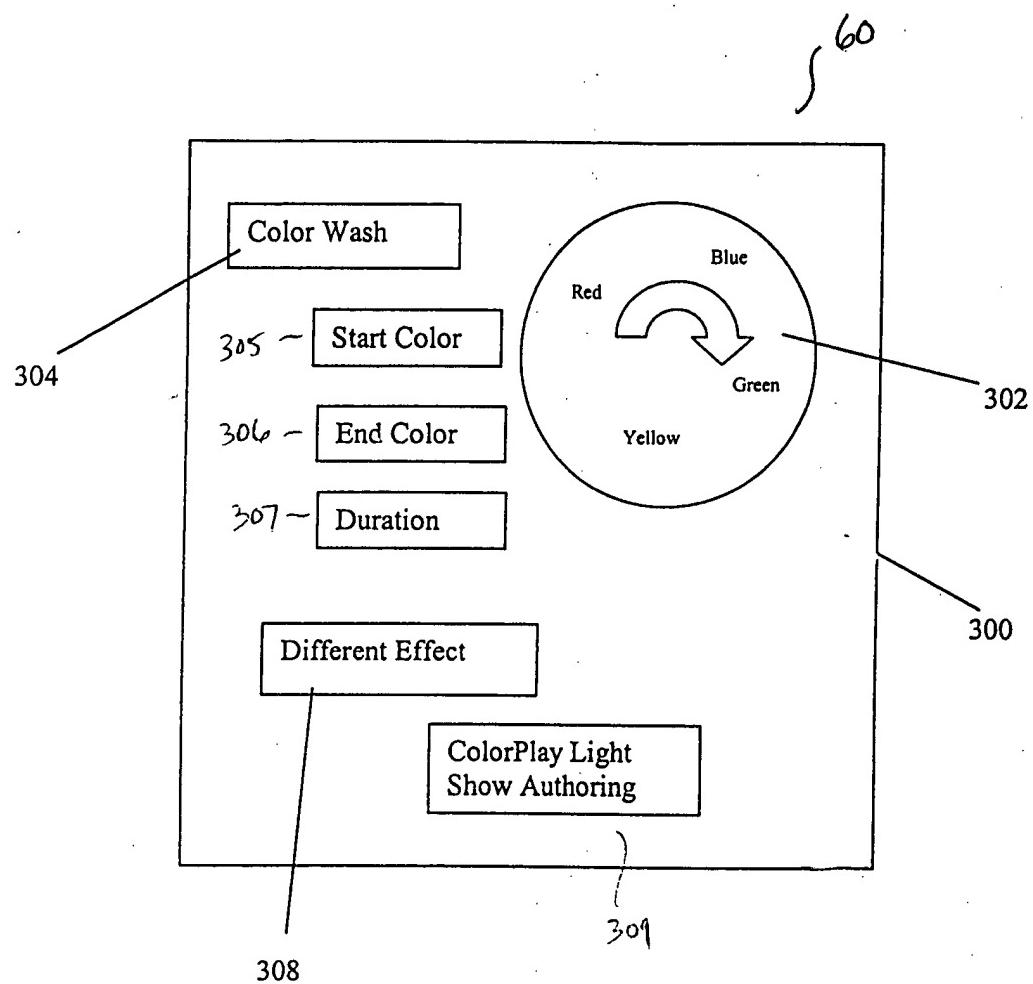


FIG. 7

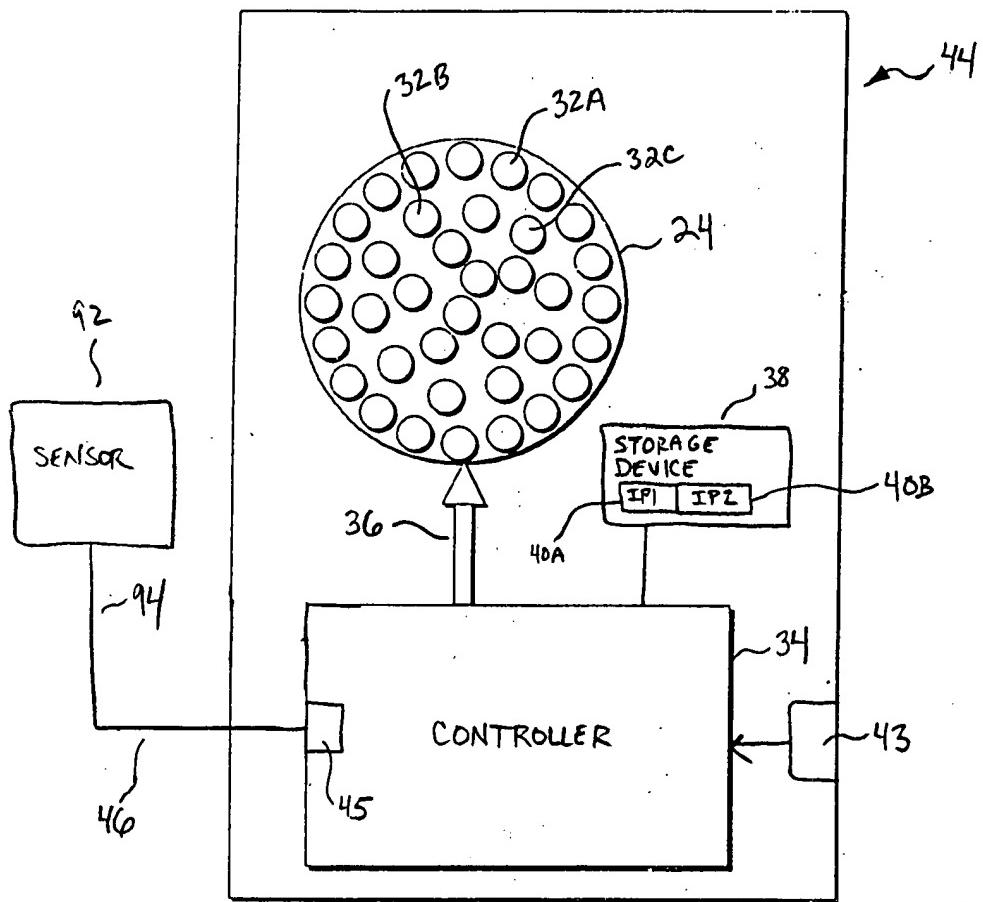


Fig. 8

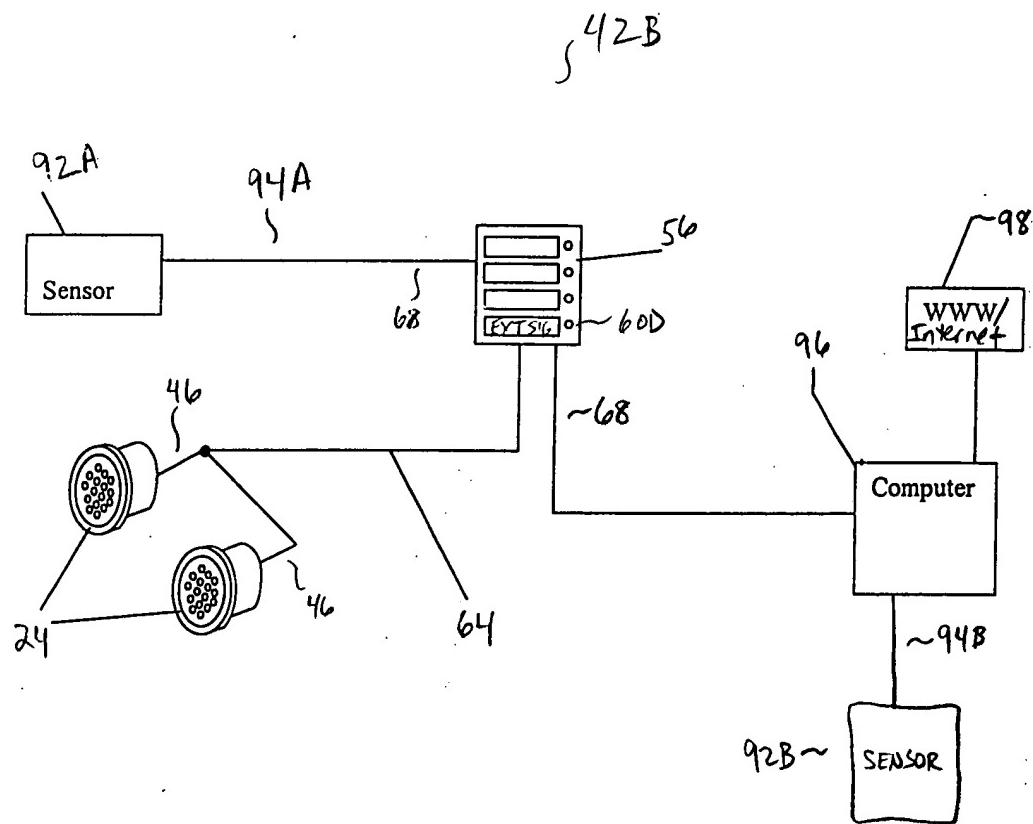


FIG: 9

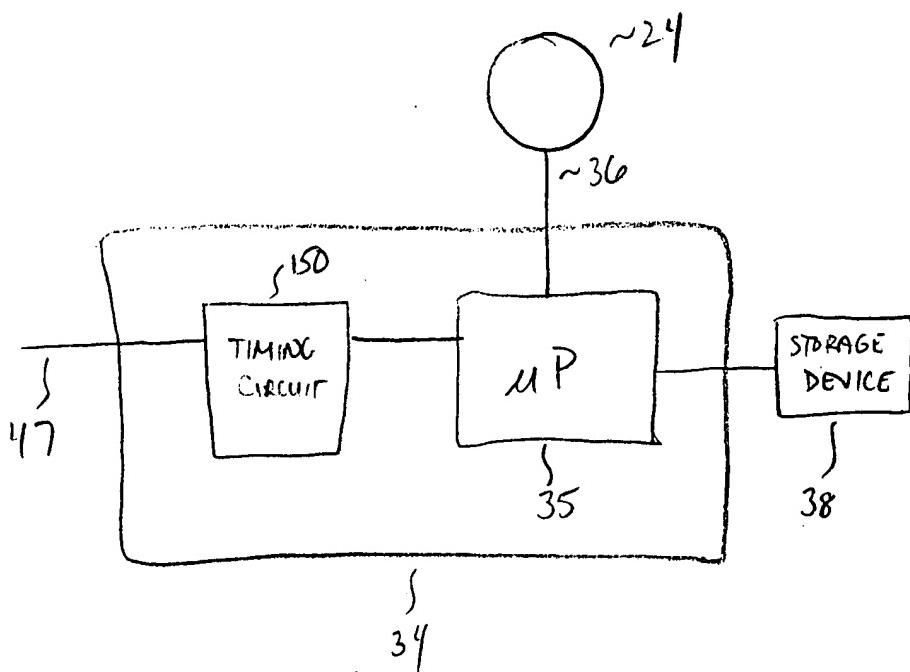


FIG. 1D

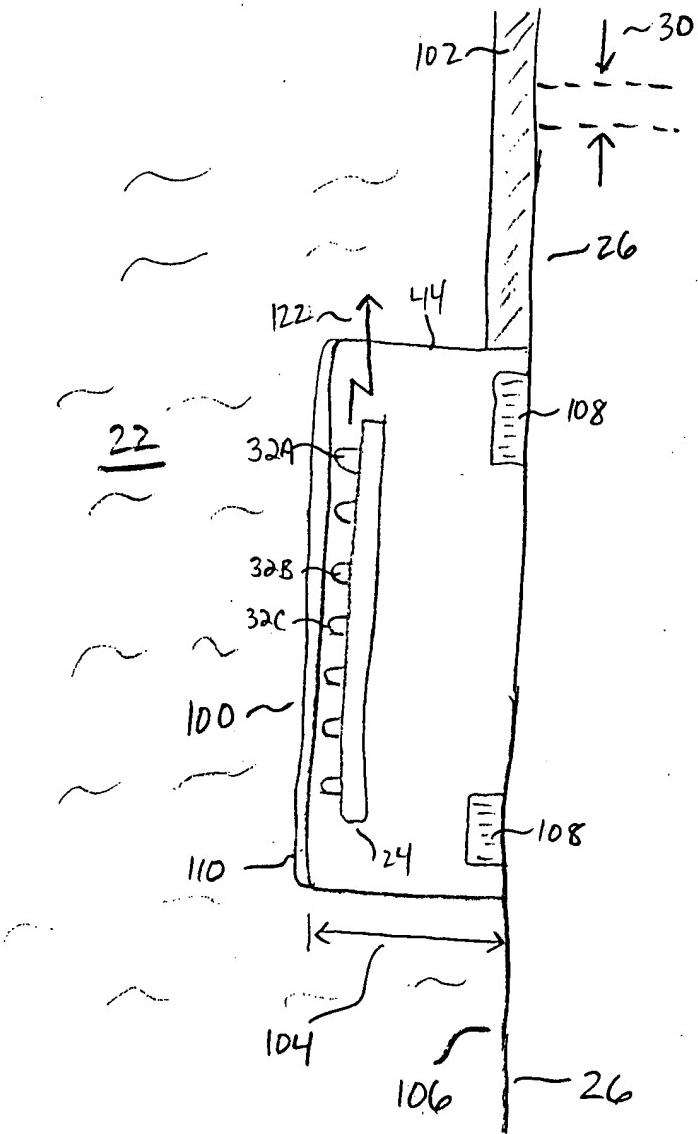
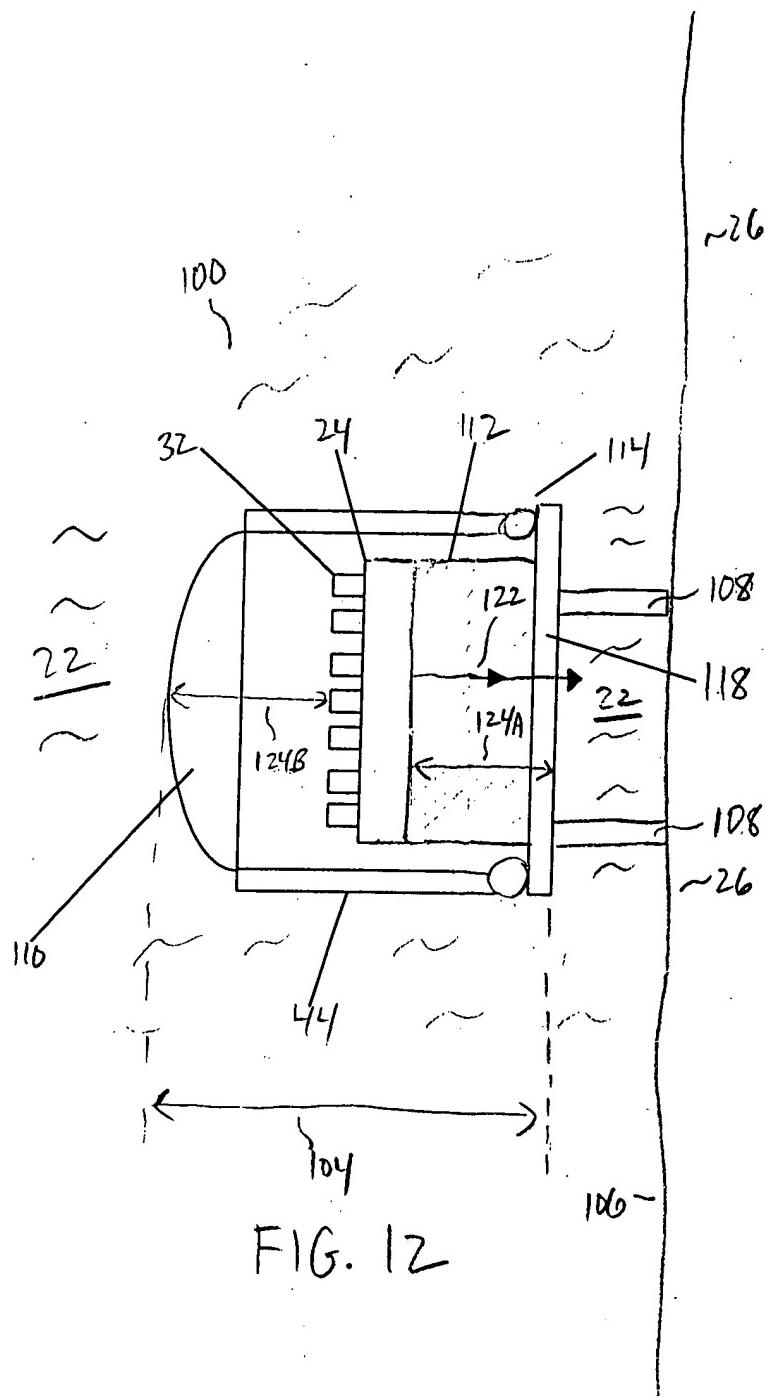


FIG. 11



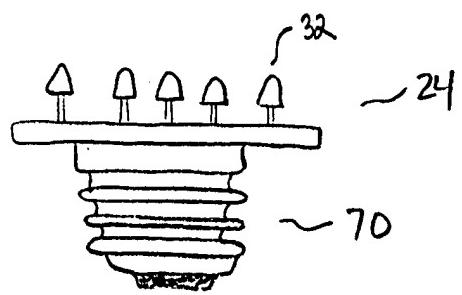


FIG. 13

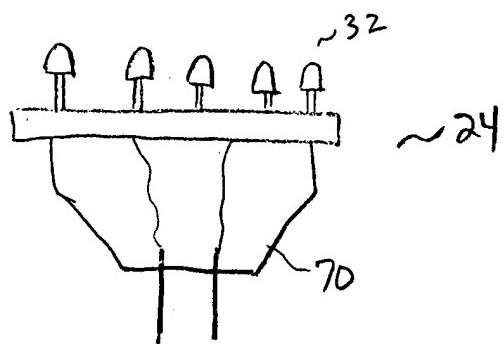


FIG. 14

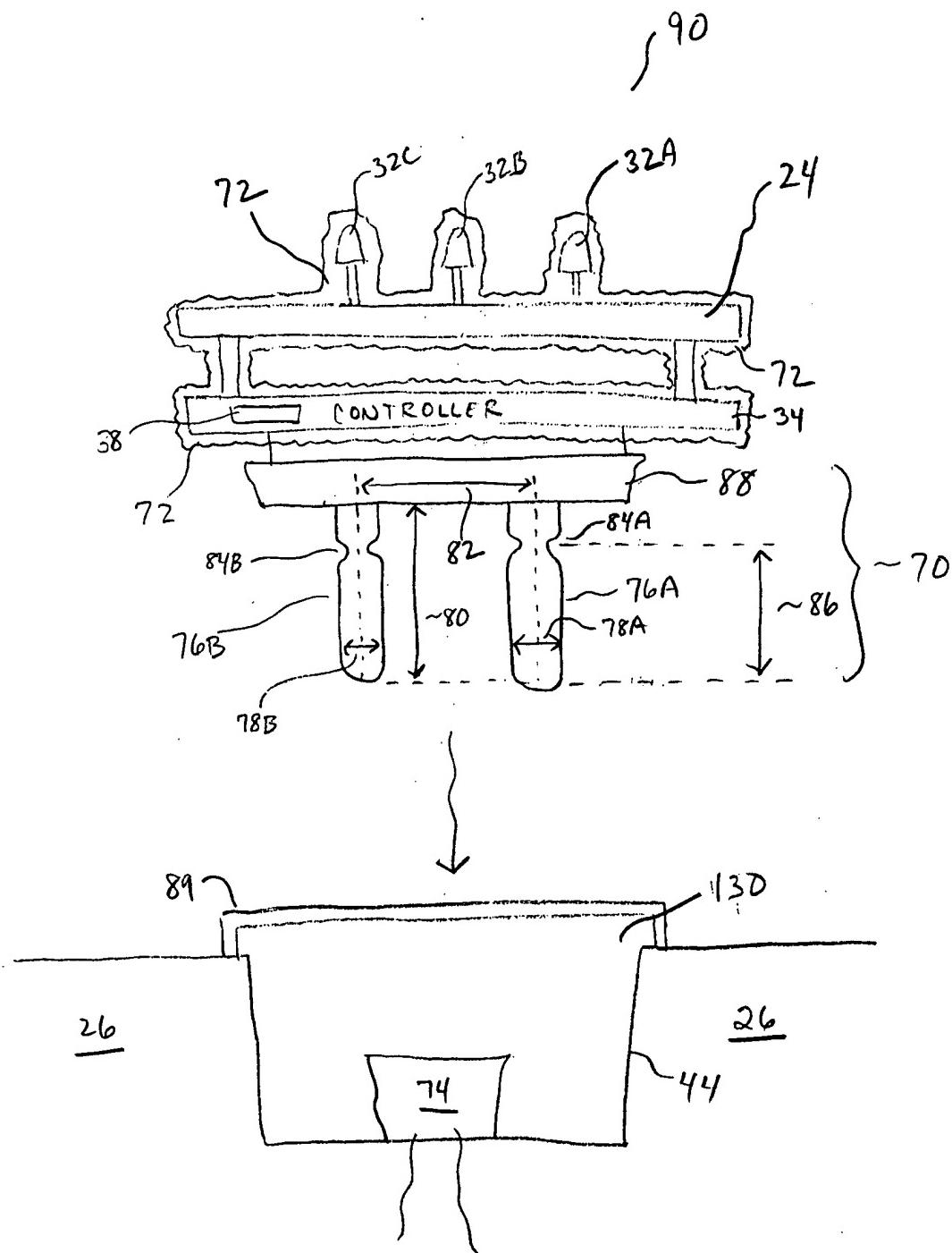


FIG. 15

4 0 0 4 5 6 2 9 - 4 0 2 5 0 1

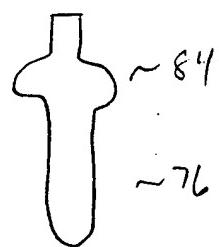


FIG. 16 A

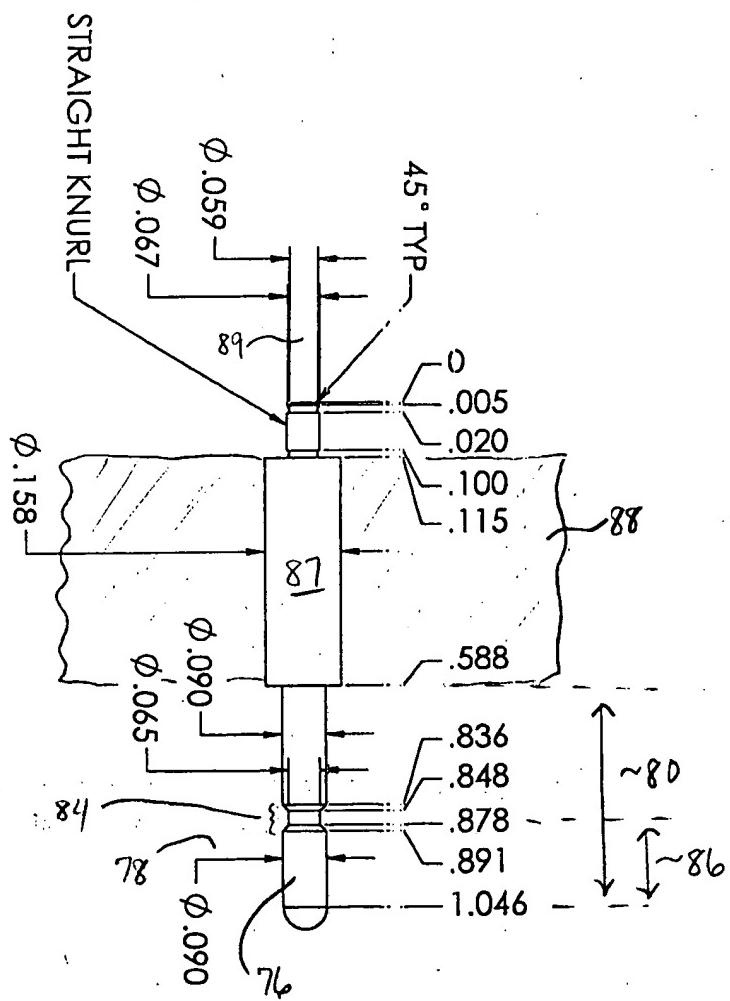
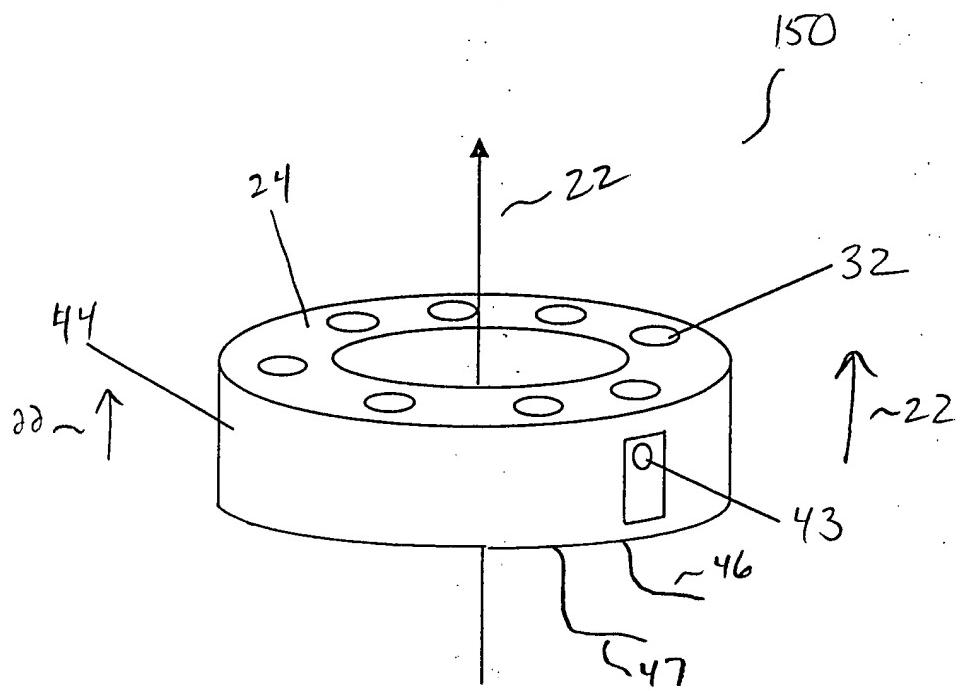


FIG. 16 B



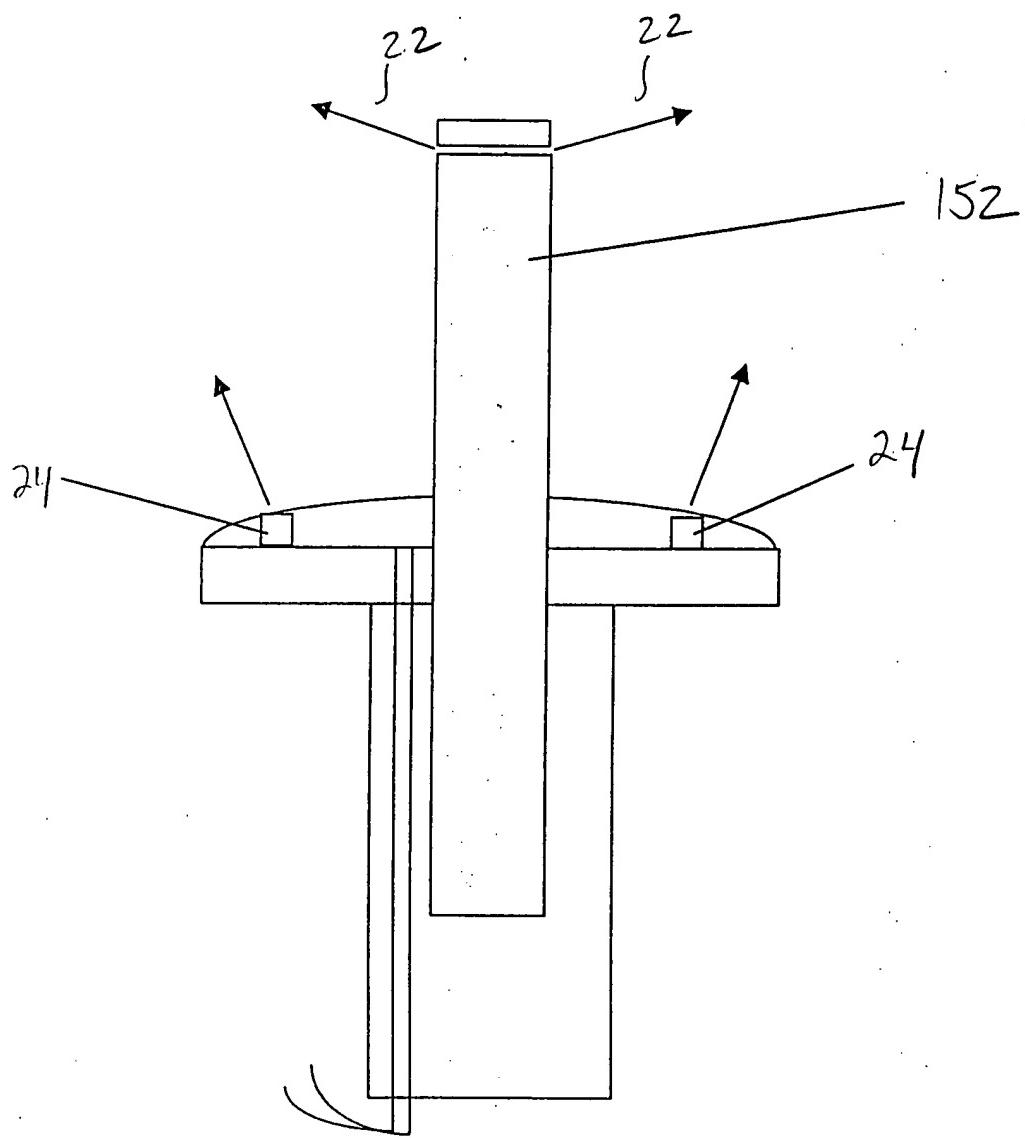


FIG. 18

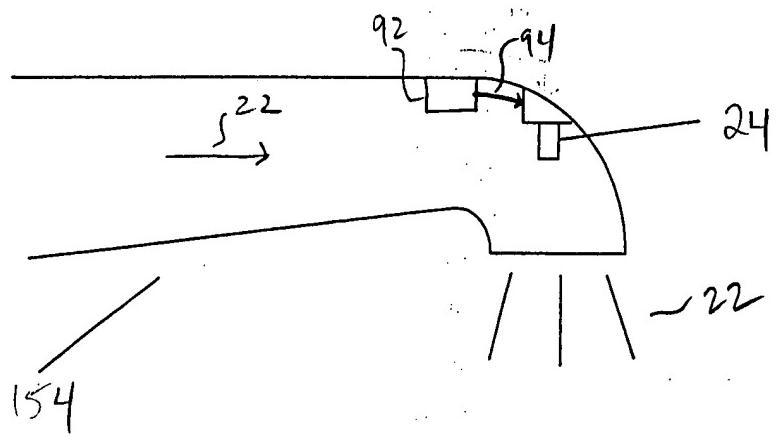


FIG. 19

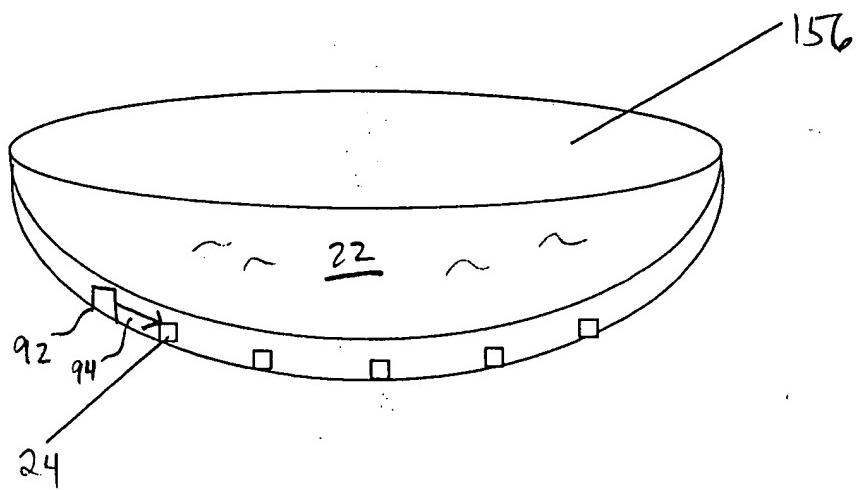


FIG. 20